

# EN0-001 Q&As

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### **QUESTION 1**

According to the AAPCS (with soft floating point linkage), when the caller "func" calls sprintf, where is the

value of the parameter "x" placed?

#include

void func(double x, int i , char \*buffer)

{

```
sprintf(buffer, "pass %d: value = %f\n", i, x); }
```

A. Split between register R3 and 4 bytes on the stack

- B. Split between registers R3 and R4
- C. 8 bytes on the stack
- D. VFP Register D0

Correct Answer: C

#### **QUESTION 2**

A 32KB 4-way set associative instruction cache supports a cache line size of 64 bytes. How many bits are required to index a cache line in a way?

- A. 6 bits
- B. 7 bits
- C. 9 bits
- D. 15 bits

Correct Answer: B

#### **QUESTION 3**

When a linker is removing unused sections during a static link (for example, -remove or -gc- sections), it finds the sections to keep by following all relocations starting from:

A. The entry point(s).

- B. The function named \\'main\\'.
- C. All local functions and variables.
- D. The reset vector.



Correct Answer: A

## **QUESTION 4**

- A re-entrant interrupt handler would typically be used to:
- A. Allow an external interrupt to interrupt an SVC handler
- B. Reduce response time for higher priority interrupts
- C. Allow an interrupt handler to be relocated in memory
- D. Avoid the need for an interrupt handler to use a stack.

Correct Answer: B

#### **QUESTION 5**

In which type of storage will the compiler preferentially place frequently accessed variables?

- A. Stack
- B. Heap
- C. Registers
- D. Hard disk
- Correct Answer: C

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