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QUESTION 1

The size of a C `int` type in the ARM architecture is:

- A. 8 bits
- B. 16 bits
- C. 32 bits
- D. 64 bits

Correct Answer: C

QUESTION 2

Which of the following processor resources do NOT have to be saved or modified by the Linux scheduler during context switch?

- A. Registers R0-R15
- B. Thread and process ID registers
- C. The CPSR
- D. NEON and VFP registers

Correct Answer: D

QUESTION 3

In the Generic Interrupt Controller (GIC) architecture, which of the following ID numbers are reserved for interrupts that are private to a CPU interface?

- A. ID0-ID7
- B. ID0-ID15
- C. ID0-ID31
- D. ID0-ID63

Correct Answer: C

QUESTION 4

When using an Operating System, which of the following operations can NOT typically be done by user processes?

- A. Reading the link register (R14)

- B. Reading data from the user stack
- C. Changing from ARM state to Thumb state
- D. Changing the interrupt mask bits (A, I, F) in the CPSR

Correct Answer: D

QUESTION 5

In which TWO of the following locations would a compiler typically place local variables? (Choose two)

- A. ROM
- B. Heap
- C. Cache
- D. Registers
- E. Stack

Correct Answer: DE

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