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QUESTION 1

Which event triggers the generation of a EVPN type-4 route?

- A. When unknown unicast traffic is received on an interface.
- B. When known unicast traffic is received on an interface.
- C. When a new VNI is configured.
- D. When a new ESI is configured.

Correct Answer: D

QUESTION 2

What are three advantages of using MPLS for data center interconnects? (Choose two.)

- A. Dedicated MPLS backbones for Layer 1 and Layer 3 DCIs
- B. Any to any connectivity
- C. Dedicated connections between customer sites
- D. Sub 50 ms failover times
- E. Traffic engineering

Correct Answer: ADE

QUESTION 3

Referring to the exhibit, which two statements are correct? (Choose two.)

```
Exhibit
(master:0)
user@switch01> show ethernet-switching vxlan-tunnel-end-point remote
Logical System Name      Id  SVTEP-IP      IFL  L3-Idx  SVTEP-Mode  ELP-SVTEP-IP
<default>                0   192.168.100.1  lo0.0  0
SVTEP-IP      L2-RTT      IFL-Idx  Interface  NH-Id  SVTEP-Mode  ELP-IP
Flags
192.168.100.2  default-switch  570      vtep.32770  1747  Man-VTEP
  VNID      MC-Group-IP
  5010      0.0.0.0
SVTEP-IP      L2-RTT      IFL-Idx  Interface  NH-Id  SVTEP-Mode  ELP-IP
Flags
192.168.100.11  default-switch  569      vtep.32769  1744  RWVE
  VNID      MC-Group-IP
  5010      0.0.0.0
```

- A. Seamless EVPN VXLAN stitching is in use.
- B. Switch01 is a spine device.
- C. An OTT data center interconnect is in use.
- D. Switch01 is a leaf device.

Correct Answer: AD

QUESTION 4

You are asked to deploy 20 QFX Series devices using ZTP Each QFX5 100 requires FTP server. In this scenario, which two component must you configure on the DHCP Server? (Choose two.)

- A. The MAC address of the FTP server
- B. The IP address of the FTP server
- C. The MAC address of each QFX5100
- D. The MAC address of each FTP QFX5100

Correct Answer: AB

QUESTION 5

You are implementing perimeter security for your data center. You need to inspect all traffic Layer 7 and ensure the failure of a port or device will not result in an interruption to traffic flows. In this scenario, which design would satisfy these requirements?

- A. MX with MC LAG

- B. QFX Virtual Chassis
- C. SRX Chassis cluster
- D. SRX using LAG

Correct Answer: C

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